

CURRICULUM VITAE

Dr. Govind Singh Patel
House No : 474/34, R K Nagar
Yadrav, Kolhapur, MH
Mobile No : 7683024738
E-mail Address : govindpatel506a2@gmail.com



Professional Qualifications:

- **Ph.D.** in Electronics and Communication Engineering from **Thapar University, Patiala, PB**, in 2016.
- M. Tech. in Instrumentation and Control Engineering from APEEJAY College of Engineering, Sohna, Gurgaon, under M.D.U. University, Rohtak, HR, in 2008 with 1st division.
- B.E. in Electronics and Communication Engineering from B.D.C.O.E. Sewagram, Wardha, under Nagpur University, Nagpur, MS, in 1998 with 1st division.

Ph.D. Students 08 : 04(Guided) + 04 (Enrolled)

Total Work Experience : 25 Years

- (a) Teaching : 22.5 Years
(b) Software Industry : 2.5 Years

Name of Organisation	Duration	Designation	Role
SITCOE, Kolhapur, Maharashtra	From 11 Sept 2021 to till date	Professor	Teaching and Research
Lovely Professional University, Jalandhar, Punjab	From 8 Jan 2018 to 24 Nov 2020	Professor	Teaching and Research
Rawal Institute of Engineering and Technology, Faridabad	From 29 Dec 2011 to 29 Dec 2017	Assoc. Professor & HOD	Administration, Teaching and Research
Lingayas University, Faridabad	From 19 Sept 2005 to 28 Dec 2011	Assistant Professor	Administration, Teaching and Research
MIT, Aurangabad & Bulandshahr	From 05 Dec 2001 to 18 Sept 2005	Lecturer	Teaching
Zedcom Infotech, Aurangabad	From 10 Jan 1999 to 04 Dec 2001	Software Developer	Programmer

ACADEMIC SUMMARY

CONFERENCES/JOURNAL PAPERS PUBLISHED

1. International Journal + SCI Journal+ Scopus Journal	22 + 14 + 14 = 50
2. International Conference + Scopus Conf.	12 + 19 = 31
3. National Conference	19
4. National Journal	01

Total	101
Consultancy done	3 Lakhs
Patents filed	07
Book published	04
Book Chapter Published	09
PROJECT GUIDANCE	
1. Ph.D.	04(Guided) 04 (Enrolled)
2. M.TECH.	12
3. B.TECH.	35
AREA OF SPECIALIZATION	Microprocessor, Microcontroller, Embedded system design and IOT.
Languages Known	VHDL, Verilog, C, Assembly etc.
Lab Manual Written as per MDU Syllabus	07

As an University Question Paper Setter

- M.V.N. University, Palwal, Faridabad
- Manav Rachna University, Faridabad

As a B. Tech. External Practical Examiner / M.Tech. Thesis Evaluator

- M.V.N. University, Palwal, Faridabad
- Manav Rachna University, Faridabad
- Shri Ram of Engineering and Technology(Under MDU, Rohtak), Faridabad

Awards and Honors:

- Received best teacher award at SITCOE in 2022-23
- Received best research paper at SITCOE in 2023
- Received best research paper at Chandigarh University in 2023

Reviewers:

Journal of Circuit, system and computer, WPC, ACCP

Head of NIRF:

I am head of NIRF committee.

ISTE Membership number: LM – 136249

Consultancy done: 3 Lakhs

Important IDs and Links:

Scopus Author ID: 42561794200

Web of Science Researcher ID: HHY-7980-2022

ORCID: <https://orcid.org/0000-0002-1671-8637>

Skype ID: govindpatel1104@gmail.com

Linked in URL: <https://www.linkedin.com/in/dr-govind-singh-patel-65363a23/>

Google scholar URL: <https://scholar.google.com/citations?user=dRTbhAcAAAAJ&hl=en>

Research gate URL: <https://www.researchgate.net/profile/Govind-Singh-Patel>

Resume URL: https://sitcoe.ac.in/images/ETC_Faculty/Dr-Govind-Singh-Patel_CV.pdf

Vidwan ID: 469624

Vidwan URL: <https://vidwan.inflibnet.ac.in/profile>

Workshop/Orientation/Symposium/Seminar Attended:

Sr.No.	Name of Course/Workshop/ Seminar/Symposium	Place	Duration	Sponsored/Organized by
1	Emerging trends in signal processing and industrial drives	LIMAT, FBD	3 to 14 July 2006	LIMAT, FBD
2	Faculty development programme	LIMAT, FBD	16 to 21 July 2007	LIMAT, FBD
3	Enhancing awareness and teaching skills	CITM, FBD	19 to 23 March 2007	ISTE-AICTE STTP & CITM, FBD
4	Audiotronics	LIMAT, FBD	16 to 20 July 2008	AICTE-ISTE & LIMAT, FBD
5	Faculty development programme	LU, FBD	08 to 10 July 2009	LU, FBD
6	PLC, SCADA	RIET, FBD	13 Sept. 2012	SOFCO, Noida
7	Embedded system & VLSI technology	RIET, FBD	4 Sept. 2012	CETPA, Noida
8	FPGA signal processing for cognitive radios	Thapar University	11 to 15 June 2012	Thapar University, Patiala
9	Training on VLSI and Embedded system design	RIET, FBD	13-15 Sept 2017	APTRON Solution Pvt Lid, Gurgoan
10	Workshop on Matlab and Verilog	RIET, FBD	14-16 March 2016	DUCAT, Noida
11	Workshop on VLSI and Embedded system design	RIET, FBD	25-27 Oct 2015	APTRON Solution Pvt Lid, Gurgoan
12	Training on Matlab and Verilog	RIET, FBD	8-10 March 2017	DUCAT, Noida
13	Workshop on VLSI and Embedded system design	RIET, FBD	9-11 Nov 2016	APTRON Solution Pvt Lid, Gurgoan
14	Workshop at Startup and IPR	LPU	29 th Aug 2019, 1 Day	LPU
15	Symposium on Professional Development & Opportunities in Electronics	IIMT	9 th May 2020, 1 Day	IIMT, Noida
16	Accreditation and outcome	NITTR	8 th March to 12 th	NITTR Chandigarh

	based curriculum design	Chandigarh	March 2021, 5 Days	
17	Low power VLSI Design	NITTR Chandigarh	5 th April to 9 th April 2021, 5 Days	NITTR Chandigarh
18	Insight to U.S Baldrige education performance excellence framework	BVVS , Bagalkot	02 nd Aug to 06 th Aug 2021	ATAL
19	Emerging technologies of Opto-VLSI and its Applications	MIET Meerut	31 st Aug to 4 th Sep 2021	MIET Meerut
20	Research Methodology and research publications and patent filing	SITCOE, Ichalkaranji	20 th Sept to 24 th Sep 2021	SITCOE, Ichalkaranji, MH
21	IoT using Arduino & Raspberry PI	Sakthi Polytechnic), MH	25 th Oct to 29 th Oct 2021	ATAL, SITCOE, Ichalkaranji, MH
22	Simulation tools of research	SITCOE, Kolhapur	08 th Feb to 14 th Feb 2022	SITCOE, Ichalkaranji, MH
23	Online Learning and Online Evaluation	NITTTR, Chandigarh	14/03/2022 to 18/03/2022	NITTTR, Chandigarh
24	NBA Accreditation and Teaching and Learning in Engineering”	NPTEL	12 Week , Jan-Apr 2021	NPTEL
25	Python, data science and machine learning	SITCOE	19 th July to 23 th July 2022	SITCOE
26	Digital circuits	NPTEL	12 week, Jan-Apr 2022	NPTEL
27	Digital Forensics	YCCE, Nagpur	5 th June to 10 th June 2023	YCCE, Nagpur
28	Industrial Automation	SITCOE	19 th Dec to 29 th Dec 2023	SITCOE
29	Recent Trends in Semiconductor Technology and Chip Design	DTU, Delhi	2 nd Dec to 7 th Dec 2024	ATAL, Delhi
30	Smart communication in iot : security & future applications and possibilities	ST.JOSEPH COLLEGE OF ENGINEERING	24/02/2025 to 01/03/2025	ATAL, Delhi
31	Recent advances and emerging trends and applications in automation and robotics	NITTE, Meenakshi Institute of technology	01/09/2025 to 06/09/2025	ATAL, Delhi
32	Introduction to IoT	NPTEL	01/09/2025 to 01/12/2025	NPTEL

Patents: 08

1. Govind Singh Patel, et al. “Intelligent Railway track detection system using FPGA”, (Application no. IN201911049486), 2019.
2. Govind Singh Patel, et al., “A smart trolley bag.(Application no. IN201911049487), 2019,

3. Govind Singh Patel, et al., “A customized universal CPLD based various IoT based applications, (Application no. IN201911053044), 2020.
4. Govind Singh Patel, et al., “A Device of smart guide system for indoor navigation”, (Application no. IN201911050730), 2020.
5. Govind Singh Patel, et al, “Optimal Control of Electromagnetic waves through Artificial Intelligent for enhancing the power,” (Application no. 202111049295), 28th Oct 2021.
6. Govind Singh Patel, et al, “an IoT based radio controlled ‘Robotic machiavellian’, terrain deminer using demining techniques” (Application no. 202241052593), 14/09/2022.
7. Govind Singh Patel, et al, “Object identification and separation technique using DSP Processor for conveyer belt” (Application no. 202321019083), 21/03/2023.
8. Govind Singh Patel, et al, “Advanced thermal management system for enhanced battery performance and longevity in electric vehicles”, (Application no. 202511005450), 07/02/2025.
9. Govind Singh Patel, et al, “Role of quantum computing in advancing electronics and communication engineering”, (Application no. 202541128978), 02/01/2026..

Book authored: 05

1. Govind Singh Patel, Smart Agriculture, Emerging Pedagogies of Deep Learning, Machine Learning and Internet of Things, CRC Press, Jan 2021, ISSN no. 9780367535803.
2. Govind Singh Patel, DIGITAL VLSI DESIGN AND SIMULATION WITH VERILOG, Wiley Publication, John Wiley & Sons, Incorporated, March 2021. ISBN. 9781119778097.
3. Govind Singh Patel et.al, Machine Learning, Deep Learning, Big Data and Internet of things for Healthcare”, CRC Press Taylor & Francis Group, Aug.2022, ISBN: 9781003227595.

4. Govind Singh Patel et.al, Advanced Computing, Communications in Computer and Information Science (CCIS, volume, 2053), Dec 2023. ISBN: 978-3-031-56700-1, 26 March 2024, Springer, Cham.
5. Govind Singh Patel et.al, “Analysis and forecasting experiment on health care time series”, LPU, Jalandhar, PB, Aug 2024, ISBBN: 978-93-94068-90-4.

Book Chapter published: 13

1. Govind Singh Patel, Frequency synthesizer and their application in signal processing, book title “ Intelligent Communication and Automation Systems”, April 2021 by CRC Press, ISBN 9780367609283.
2. Govind Singh Patel, “IOT in Agriculture: Survey on Technology, Challenges and Future Scope”, Emerging Pedagogies of Deep Learning, Machine Learning and Internet of Things, CRC Press, ISSN no. 9780367535803, Jan 2021.
3. Govind Singh Patel, “Classification of segmented image using increased global contrast for paddy plant disease” Emerging Pedagogies of Deep Learning, Machine Learning and Internet of Things, CRC Press, ISSN no. 9780367535803, Jan 2021.
4. Govind Singh Patel and Ashish A. Mulajkar, “A single cycle low latency bypass router based on network on chip”, ICICS Proceeding, CRC press (Taylor & Francis), ISBN: 978-1-00-312910-3, DOI: 10.1201/9781003129103.
5. Govind Singh Patel et. al “EMG-based Robot control Human”, Machine learning, deep learning, big data, and internet of things for healthcare”, August 2022, CRC Press. ISBN: 9781003227595, DOI: [10.1201/9781003227595]

6. Govind Singh Patel et. al “Role of IoT in sustainable Healthcare”, Machine learning, deep learning, big data, and internet of things for healthcare”, August 2022, CRC Press. ISBN: 9781003227595, CRC Press., DOI: [10.1201/9781003227595]
7. Govind Singh Patel et. al, “Analysis and Implementation of Driver Drowsiness, Distraction, and Detection System”, ISBN: 978-3-031-56700-1, 26 March 2024, Springer, Cham, <https://doi.org/10.1007/978-3-031-56700-14>.
8. Govind Singh Patel et. al, “ Basic of time series and forecasting for healthcare”, ”, LPU, Jalandhar, PB, pp-1-22, Aug 2024, ISBBN: 978-93-94068-90-4.
9. Govind Singh Patel et. al, “ Modeling and forecasting of healthcare expenditure using different time series analysis”, ”, LPU, Jalandhar, PB, pp-227-249, Aug 2024, ISBBN: 978-93-94068-90-4.
10. Govind Singh Patel et. al, “ ARMA and ARIMA process for forecasting”, ”, LPU, Jalandhar, PB, pp-209-226, Aug 2024, ISBBN: 978-93-94068-90-4.
11. Govind Singh Patel et. al., “Enhancing healthcare efficiency through edge computing: empowering healthcare with edge computing”, IGI Global Scientific publication, DOI: 10.4018/979-8-3373-0781-7.ch017, ISBN: 9798337307817, 2025.
12. Govind Singh Patel et. al, “IoT-Enabled Energy Monitoring and Management Systems”, pp-255-273, Aug 2025.
13. Govind Singh Patel et. al, “The Role of Internet of Things (IoT) in Hybrid and Renewable Energy Systems”, pp. 99-125. Aug 2025.

NPTEL Courses:

1. Govind Singh Patel, “Digital System Design”, NPTEL online certificate, 12 week course (Jan-Apr 2021).
2. Govind Singh Patel, “ NBA Accreditation and Teaching and Learning in Engineering”, NPTEL online certificate, 12 week course (Jan-Apr 2022).
3. Govind Singh Patel, “ Introduction to IoT”, NPTEL online certificate, 12 week course (Aug -Dec 2025).

List of publications:

Total No of Publications: 50+1+31+19=101

International Journals:[50]

1. Govind Singh Patel and Sanjay Sharma, “Parametric analysis of a novel architecture of Phase Locked Loop for communication system, **International Journal of Wireless Personal Communication, Springer**, Vol. 77(2), pp. 1271-1285, 2014 (**SCI Indexed by Thomson Reuters; Impact Factor: 0.979**).
2. Govind Singh Patel and Sanjay Sharma, “Parametric analysis to reduce phase noise of frequency synthesizers for wireless communication system, **International Journal of Wireless Personal Communication, Springer**, vol. 75(2), pp. 1295–1306 (**SCI Indexed by Thomson Reuters; Impact Factor: 0.979**).
3. Govind Singh Patel and Sanjay Sharma, “FPGA-based Direct Digital Synthesis Analysis and Implementation,” **Journal of Computational and Theoretical Nano-Sciences, American Scientific Publishers, United States of America**, vol.10(10), pp. 2458-2461 (2013). (**SCI Indexed by Thomson Reuters; Impact Factor: 0.91**).

4. Govind Singh Patel and Sanjay Sharma, "Predicting the Jitter of Phase Locked Loop-DLL based Frequency synthesizers", **International Journal of Wavelets, Multiresolution and Information Processing, World scientific Publication**, Vol. 12(2), (2014),pp. 1-10. (SCI Indexed by Thomson Reuters; Impact Factor: 0.50).
5. Govind Singh Patel and Sanjay Sharma, "The Optimization of Direct Digital Frequency Synthesizer Performance by New Approximation Technique," **Research Journal of Applied Sciences, Engineering and Technology, Maxwell Scientific Organization**, 5(11): 3134-3139, 2013.(Thomson Reuters Zoological Records).
6. Govind Singh Patel and Sanjay Sharma, "Frequency synthesis techniques for high speed communication system," **International Journal of the Physical Sciences, Academic Research publication**, 6(11), pp. 2618 -2632, 4 June, 2011. (Formerly SCI Indexed when published by Thomson Reuters; Impact Factor: 0.40).
7. Govind Singh Patel and Sanjay Sharma, "Field Programmable Gate Array- based Analysis, Design and Implementation of ADPLL," **Journal of Computational and Theoretical Nano-sciences, American Scientific Publishers**, United States of America, Vol. 11(7), pp. 1-7, 2014. (SCI Indexed by Thomson Reuters; Impact Factor: 0.91).
8. Govind Singh patel & Sanjay sharma,"Design of an All Digital PLL(ADPLL) IP core using FPGA", International e-Journal of Communication and Electronic, vol. 3, page-11-21, March 2009.
9. Govind Singh patel, Amrita Rai,"Multiple Traffic Control Using Wireless Sensor and Density Measuring Camera", International Frequency Sensor Association, Vol. 94, page-126-132, July 2008.
10. Govind Singh Patel, S V A V Prasad & Amrita Rai,"Artificially Controlling the Limb Movement of Robotic Arm Using Machine Interface With EMG Sensor", International Frequency Sensor Association, Vol. 89, page-39-51, March 2008.
11. Govind Singh patel & Sanjay sharma, "Comparative study of DDS, PLL and DDS based PLL .,""comparative analysis of different pll synthesis techniques for communication system", 13IEEMS, Volume (5), September 2010, pp. 38-49.
12. Govind Patel and Sanjay Sharma, "Comparative analysis of different pll synthesis techniques for communication system", International Electronic Engineering Mathematical Society IEEMS, Volume (5), September 2010, pp. 38-49.
13. Govind Singh Patel , Sarita Zutasi & Arvind pathak, " Reduction of Settling time in frequency synthesizers using Bandpass filters ", International Journal of new Innovations , MVN, vol . 1, pp. 101-103, Feb 2012.
14. Govind Singh Patel, Sanjeet Kumar Sinha & Suman Lata Tripathi, "Mole-Fraction Engineering in Germanium Source Pocket Based Tunnel Field Effect Transistor", Sensor letters International Journal, American Scientific Publishers, Vol. 17(6), pp. 470–473, June 2019.(**Scopus Index Journal**).
15. Govind Singh Patel and Nripendra Narayan Das, Privacy and Security in Online Social Media-a Big Challenge, International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-8 Issue-6, April 2019.(**Scopus Index Journal**).
16. Govind Singh Patel and Suman Lata Tripathi, "Design Low Power Pocket Si_{0.7}Ge_{0.3} Junction-less Tunnel FET using below 5nm Technology," **International Journal of Wireless Personal Communication, Springer, Nov 2019 (SCI Indexed by Thomson Reuters; Impact Factor: 0.979)**. (DOI: 10.1007/s11277-019-06978-8).
17. Govind Singh Patel, Nripendra Narayan Das, Sanjeet Kumar Sinha, "Optimization of Frequency Settling Time of Pll using 3 Rd Mash Sigma Delta Modulator," International Journal of Innovative Technology and Exploring Engineering (**Scopus Journal**), Volume-9 Issue-3, January 2020.
18. Ashish A Mulajkar, Govind S Patel, "3D Torus Router Architecture For Efficient Network on Chip Design", Think India Journal, Vol-22-Issue-16-August-2019.

19. Govind S Patel, Suman L Tripathi, Sanjeet K Sinha and Sobhit Saxena,” Design and Analysis of Power an Efficient Hybrid PLL For Communication System”, T. India Journal, Vol-22-Issue-16-August-2019.
20. Sanjeet K. Sinha, Sweta Chander, Govind S. Patel,” Application of Graphene as Carbon Nanotube For Future CNTFET Devices,” T. India Journal, Vol-22-Issue-16-August-2019.
21. Sobhit Saxena, Suman L. Tripathi, Sanjeet K. Sinha, Govind S. Patel and Chepuri Pravalika, “Review on Performance Evaluation of TFET Structures & Its Applications,” T. India Journal, Vol-22-Issue-16-August-2019.
22. Suman Lata Tripathi , Arti Singh, Sajeet Kumar Sinha, Sobhit saxena and Govind Singh Patel,” Performance Evaluation of Advance LED Structures and Applications,” T. India Journal, Vol-22-Issue-16-August-2019,
23. Suman Lata Tripathi , Kadi Jaya Ramesh, Sobhit saxena, Sajeet Kumar Sinha and Govind Singh Patel,” Review on Tunnel Field Effect Transistor For Biomedical Applications,” T. India Journal, Vol-22-Issue-16-August-2019.
24. Sandeep K Shelke, Govind Singh Patel and Bhupinder Verma,” Analysis of Complete Image Processing System and Design of Improved Image Compression System,” T. India Journal, Vol-22-Issue-16, pp. 1161-1169,-August-2019.
25. Sandeep K Shelke and Govind Singh Patel, “Low Power High Frequency Implementation of Image Filtering using Improved Median Filtering”, International Journal of Advanced Science and Technology Vol. 29, No. 4s, April 2020, pp. 1833-1843.(**Scopus indexed**)
26. Sheetal Nagar, Govind Singh Patel and Seema Nayak, “A Review paper on “IOT” & It’s Applications,” Journal of Information and Computational Science(UGC care journal), Volume 13 Issue 4, April 2020.
27. Govind Singh Patel and Suman Lata Tripathi, “Low-Power Efficient p+ Si_{0.7}Ge_{0.3} Pocket Junctionless SGTFTFET with Varying Operating Conditions,” **Journal of Electronic Materials**, 49(7), pp.4291–4299 (2020).;SCI: IF=1.67,April 2020. (**SCI Indexed by Thomson Reuters; Impact Factor: 1.67**). [.https://doi.org/10.1007/s11664-020-08145-3](https://doi.org/10.1007/s11664-020-08145-3).
28. Ashish A Mulajkar, Govind S Patel, “Performance Analysis of various Parameters of Network-on-chip (NoC) for different Topologies”, JETIR, Volume 6, Issue 1, January2019. (published in Non 2020, **UGC care listed**),
29. Ashish A Mulajkar, Govind S Patel, “VC Router Design For Power Efficient Network On Chips”, JETIR, Volume 6, Issue, January 2019.
30. Ashish A Mulajkar and Govind Singh Patel, “Easy Pass Router Design for Power Efficient Network on Chips”, international journal of research culture society”, July 2020. (Published)
31. Govind singh patel, “FPGA implementation of the fractional frequency synthesizers”, 2018 JETIR December 2018, Volume 5, Issue 12(published in Nov 2020). (**UGC care listed journal**).pp 371-377.
32. Sheetal Nagar1 , Govind Singh Patel , Seema Nayak, “Design Of Low Power Digital Multiplier”, 2019 JETIR January 2019, Volume 6, Issue 1, pp. 1392-1399. Published in Nov 2020. UGC care listed.
33. Govind S Patel, et. al, “Displaying Image through VGA Cable using Verilog”, International Journal of Engineering Research in Electronics and Communication Engineering, Vol 8, Issue 9, September 2021.
34. Govind Singh Patel et. al, “Study of End to End Image Processing System including Image De-noising, Image Compression & Image Security,” **International Journal of Wireless Personal Communication, Springer, 121, 209–220 (2021).**, Nov. 2021 (**SCI Indexed by Thomson Reuters; Impact Factor: 1.061**). [.https://doi.org/10.1007/s11277-021-08631-9](https://doi.org/10.1007/s11277-021-08631-9)
35. Govind Singh Patel et al. “Analysis of improved ez-pass router design and its FPGA implementation”, Journal of Harbin Institute of Technology, Vol. 53 Issue. 11 Pp. 17-28, Dec 2021(**SCOPUS**).

36. Govind Singh Patel et al., “Development of improved speed AES based on key dependent intra-round operations”, *Journal of Theoretical and Applied Information Technology*. 99(13), ISSN No. 1992-8645, pp 3231- 3241, 2021. (SCOPUS).
37. Govind Singh Patel et al., “Design Of Power, Area and Delay Optimized Direct Digital Synthesizer Using Modified 32-Bit Square Root Carry Select Adder”, **Journal of Circuits, Systems and Computers**, World Scientific Publication(SCI Indexed by Thomson Reuters; Impact Factor: 1.33), Sept 2022. <https://doi.org/10.1142/S0218126622502929>.
38. Govind Singh Patel et al., “An Ultra-Low-Power C-Band FMCW Transmitter Using a Fast Settling Fractional-N DPLL and Ring-Based Pulse Injection Locking Oscillator”, **Journal of Circuits, Systems and Computers**, World Scientific Publication(SCI Indexed by Thomson Reuters; Impact Factor: 1.33), Nov 2022. <https://doi.org/10.1142/S0218126623500457>.
39. Govind Singh Patel et al. “A comparative study on approximate low power multiplier”, *Journal of east china university of science and technology*, Vol.-65 ISS. - 4, pp. 578-586, DEC. 2022. (Scopus Indexed).
40. Govind Singh Patel et al., “A Design and Investigation Inexact compressor based on low power Multiplier”, **Journal of Circuits, Systems and Computers, World Scientific Publication (SCI Indexed by Thomson Reuters; Impact Factor: 1.33)**, Aug. 2023. <https://doi.org/10.1142/S0218126623502985>.
41. Govind Singh Patel et. al, “Analysis and Implementation of Driver Drowsiness, Distraction, and Detection System”, 13th International Conference, IACC 2023, Kolhapur, India, December 15–16, 2023.
42. Govind Singh Patel et. al “Analysis of CSVCO with different low power techniques for improved performance of PLL”, *Journal of Technology*, VOLUME 12 ISSUE 9, pp. 456-463, Sept 2024. (Scopus Indexed). DOI:18.15001/JOT.2024/V12I9.24.709.
43. Govind Singh Patel, Revati Madke, Alfija, “Design and Development of Robotic Grippers for Safety Manipulating Medical Tools, with Enhancing Precision in Healthcare Procedures”, *INDIAN JOURNAL OF TECHNICAL EDUCATION* Volume 47 Special Issue 1 No. 1 1 August 2024. (UGC Care Journal).
44. Govind Singh Patel, Rushikesh, Janmanjay Desai, “Web 3.0 Technologies in Education, Innovation and Research”, *INDIAN JOURNAL OF TECHNICAL EDUCATION* Volume 47 Special Issue 1 No. 1 1 August 2024. (UGC Care Journal).
45. Govind S.Patel et. al., “The Role of Composite Materials in Electric Vehicles: Enhancements in Performance, Safety, and Efficiency”, **Journal of Polymer and Composites**, vol. 13(2), pp. S296-S310, 2025.(ESCI Journal). <https://www.webofscience.com/wos/woscc/full-record/WOS:001491404900002>
46. Govind S. Patel et. al., “Advancement in polymer composites for hybrid electric vehicles: Market potential and future directions”, **Journal of Polymer and Composites**, vol. 13(2), pp. S416-S431, 2025.(ESCI Journal). <https://www.webofscience.com/wos/woscc/full-record/WOS:001491303100002>
47. Govind S Patel, Abdul Muqeen & B Arun Kumar, “A review article on fractional N-PLL for frequency synthesizer”, **Indian Journal of Technical Education (IJTE) Special Issues**. Vol- 48, no-1, pp. 326-231, Nov .2025. ISSN 0971-3034.
48. Govind S Patel and Rahul Joshi, “Enhancing Object Sorting Efficiency: A Survey of Robotic Arm Systems Employing Image 345 Processing and Machine Learning”, **Indian Journal of Technical Education (IJTE) Special Issues**. Vol- 48, no-1, pp. 354-352,Nov. 2025. ISSN 0971-3034.

49. Govind S.Patel & Sunil Kumar Gupta, “Green energy and intelligent transportation: The role of composite materials in electric and automotive sectors”, **Journal of Polymer and Composites, Accepted: Dec. 2025(ESCI Journal).**

50. Govind S.Patel & Sunil Kumar Gupta, “Automation- driven composites:Poineering the next generation of lightweight and sustainable electric vehicles”, **Journal of Polymer and Composites, Accepted: Dec. 2025(ESCI Journal).**

[B]National Journals:[1]

1. Govind Singh Patel & Sanjay Sharma , “DDS-Based Hybrid PLL synthesis technique for communication”, LINGAYAS Journal of Professional Studies, Lingayas University, Vol. 5, No.1,pp-14-19 July-Dec, 2011.

[C] International Conference:[31]

1. Govind Singh patel & Amrita Rai, "Extended: Character Recognition using Java Object Oriented Neural Engine", International Conference, Ghaziabad, page-127-136, Feb. 2009.2
2. Govind Singh patel & Amit Garg, "Extended: Robotics- an integrated system controlled by Artificial Neural Network", International Conference, Ghaziabad, page-36-44, Feb. 2009.2
3. Govind Singh patel & Amrita Rai, ""Extended: Global traffic control by wireless sensor and density measuring camera", International Conference, Ghaziabad, page-72-78, Feb. 2009.
4. Govind Singh patel "Extended: Robotics- An Intelligent System: An Overview", International Conference on InnovativeTechnologies(ICIT-09),PDM, Bahadurgrah [HR], June 2009.
5. Govind Singh patel, S V AV prasad & Amrita Rai, ""Implementation and Design of FET Library using ASIC-VLSI",International Conference on Innovative Technologies (ICIT-09),PDM, Bahadurgrah[HR], June 2009.ST4 P-450.
6. Govind Singh patel & Ashish Siwach,“DDFS Implementation on VERILOG”, International Conference on Advanced Computing and Communication Technologies (ACCT 2011), Rohtak, 2010, pp.-393-396.
7. Govind singh patel & Sanjay Sharma, “ Analysis and Reduction of Noise in Fractional PLL “,IS3C 2012, Taiwan, June 2012.
8. Govind Singh Patel, Shelly B & Kritika, “Reduction of Phase Noise in PLL”, International Conference on VLSI, MEMS, NEMS (VMN-2012), Jan 2012.
9. Govind Singh Patel, Kritika & Shelly B, “Implementation of low noise, wide Range ADPLL”, International Conference on VLSI, MEMS, NEMS (VMN-2012) Jan 2012.
10. Rachna and Dr. Govind S Patel, “Compressed sensing: techniques for efficient acquiring and reconstructing a signal using reconstruction algorithm”, International conf. on Innovations in Electronics and Communication design, Faridabad, May 2017.
11. Dr. Govind Singh Patel, Mathematical and Behaviors Approach to reduce Phase Noise of Frequency Synthesizer, IEEE, International Conference on “Intelligent Circuits and Systems ICICS 2018, LPU, Jalandhar, PB, April 2018.
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Dr. Sanjay Sharma Professor Department of ECE Thapar University, Patiala Email id : sanjaysharma71@gmail.com	Dr. Ashok Arora VC, MMM University, Ambala, HR Ambala Email id : ashok_123@yahoo.com
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Date :

Place : Kolhapur

(Dr. Govind Singh Patel)